

**International Conference on Semiconductor Chip Design and Manufacturing (SCDM2025)**  
**School of Engineering**  
**Indrashil University**  
**Program Schedule**  
**Date: 20-06-2025**

Time		
9.00 AM	<b>Registration and Breakfast</b>	
	<b>Guests and Speaker</b>	<b>Topic</b>
	<b>Inaugural Program</b>	
9:30 AM to 10:55 AM	<b>Dr Nilesh Desai</b> <b>Director, SAC, ISRO</b> <b>Ms Nehakumari</b> <b>MD, GSEM, DST, Govt. of Gujarat</b> <b>Dr Philippe Mourin,</b> <b>Scientific and University Attache, Consulate General of France in Mumbai</b> <b>Guest of Honor - Tejas Vaghela</b> <b>Senior Director and CTO, SLS (India) Pvt Limited</b>	<b>Key Note Addresses</b>
	<b>Plenary Talk</b>	
11:00 AM	Prof. V Ramgopal Rao Group Vice Chancellor, BITS, India (Online)	Advancing IoT Sensor Networks in the Developing World: The Role of MEMS/NEMS Platforms in Overcoming Key Challenges
11:30 AM	Prof. Kuei-Shu Chang-Liao, National Tsing Hua University, Taiwan (Online)	High Performance High-k Gated SiGe/Ge CMOS Devices with Low Temperature Process
11:50 AM	Kashyap Jani, Senior Director of Technology, Qualcomm, USA	Silicon Testing and Manufacturing: Strategies and Challenges
12:15 AM	Jwalant Joshipura, Senior Director, NXP Semiconductors, Bangalore, India	Advances in Semiconductor ICs and Systems: Transforming Automotives to Robots on Wheels
12:40 PM	Dr Nihar Ranjan Mohapatra, Professor, IIT Gandhinagar	Nanoscale MOS Transistors: Progress, Dead Ends and New Horizon
	<b>Invited Talk</b>	
2:15 PM	Nirmal Soni, Design Engineer, System Level Solutions, V. U. Nagar, Anand	Neuromorphic Chip Design: Bridging Brain-Inspired Architectures with Ultra-Low Power Computing
3:00 PM	Shantanu Sinha, Head, RF Devices and Package Development Division, SAC, ISRO, India	High Frequency Semiconductor Device and Packaging technologies for Space
	<b>Research Paper Presentation/Panel Discussion</b>	
3:10 PM To 4:30 PM	Moderator: Nilesh Ranpura – elnfochips, Ahmedabad Kashyap Jani- Qualcomm, USA Jwalant Joshipura- NXP Semiconductors Nihar Ranjan Mohapatra – IIT Gandhinagar Shantanu Sinha - SAC, ISRO	Semiconductors: Opportunities and Challenges for Developing India